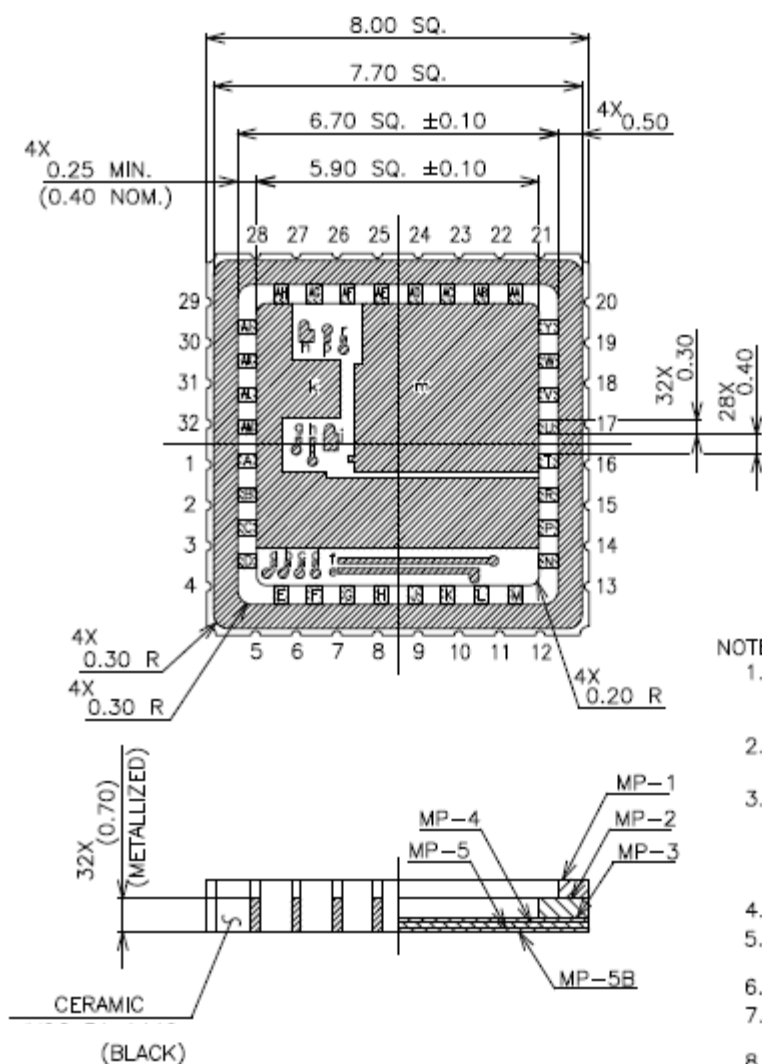
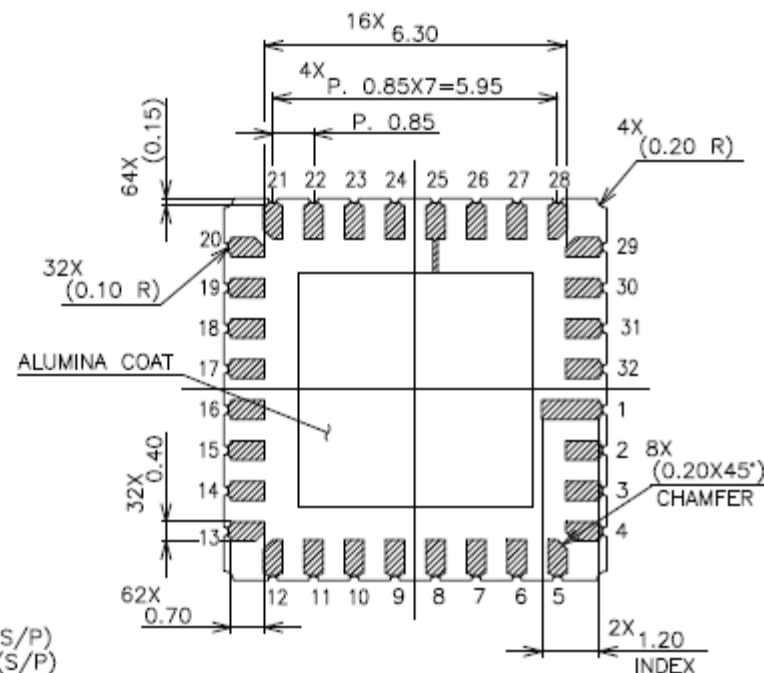




NOTES:

- PLATING THICKNESS
 ELECTRO Ni : 1.27~8.89 μ m(S/P)
 ELECTRO Au : 0.30~1.00 μ m(S/P)
- SEAL AREA CAMBER, D/A CAMBER, CERAMIC CAMBER : SEE SHEET 2/8.
 (MEASUREMENT POINT : SEE FIGURE 1, FIGURE 2, FIGURE 3)
- CONNECTION
 (A-1),(B-2),(C-3),(D-4),(E-5),(F-6),(G-7),(H-8),(J-9),(K-10),(L-e-11),(M-AK-f-j-12-30),(N-13),
 (P-14),(R-15),(T-16),(U-17),(V-d-r-18),(W-c-p-19),(Y-20),(AA-21),(AB-b-h-22),(AC-a-g-23),
 (AD-m-24),(AE-25),(AF-k-26),(AG-27),(AH-n-28),(AL-31),(AM-32),(AJ-29-S/R)
- TOTAL THICKNESS SHALL BE INCLUDING METALLIZATION AND EXCLUDING CAMBER.
- EXCESS Au PLATING ON ALUMINA COAT SHALL BE ACCEPTED.
 INSULATION OF ALUMINA COAT SHALL NOT BE REQUIRED.
- PATTERN EXPOSURE BY ALUMINA COAT MISALIGNMENT SHALL BE ALLOWED.
- ELECTRICAL OPEN AND SHORT CHECK SHALL NOT BE REQUIRED
 AS LOT TOLERANCE PERCENT DEFECTIVE 0.1% PER TAPE LOT IN SINGLE UNIT.
- HERMETICITY CHECK SHALL BE DONE BY SAMPLING
 AS LOT TOLERANCE PERCENT DEFECTIVE 0.1% PER TAPE LOT IN SINGLE UNIT.
- DIMENSIONS IN PARENTHESIS () INDICATE DESIGN VALUE AND ANY TOLERANCE NOT APPLICABLE.



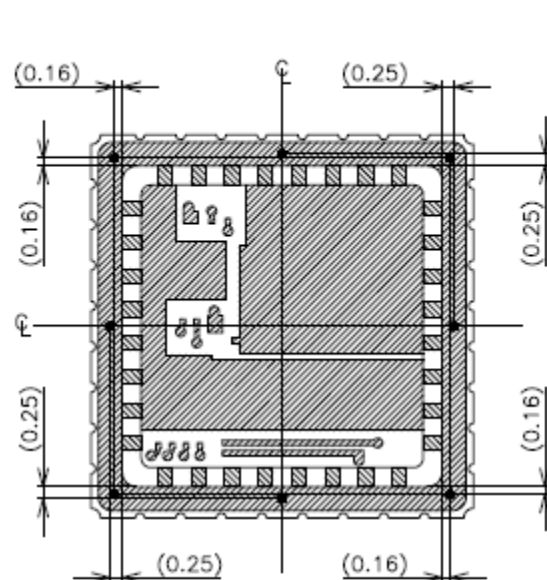


FIGURE 1
(NOTE 2)

[MEASUREMENT ITEM]
SEAL AREA CAMBER

[STANDARD]
80μm MAX.

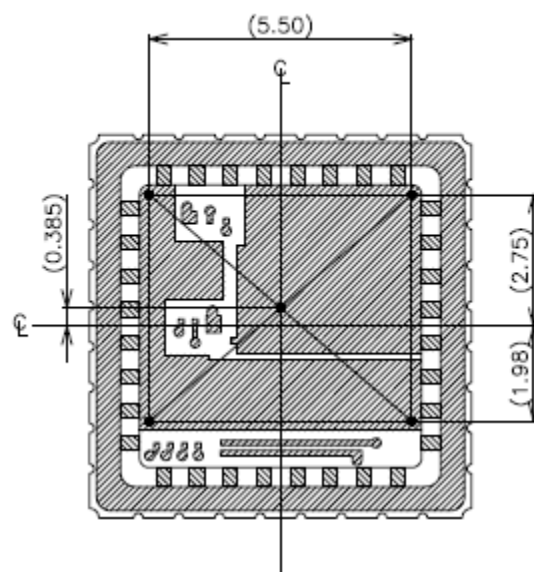


FIGURE 2
(NOTE 2)

[MEASUREMENT ITEM]
D/A CAMBER

[STANDARD]
80μm MAX.

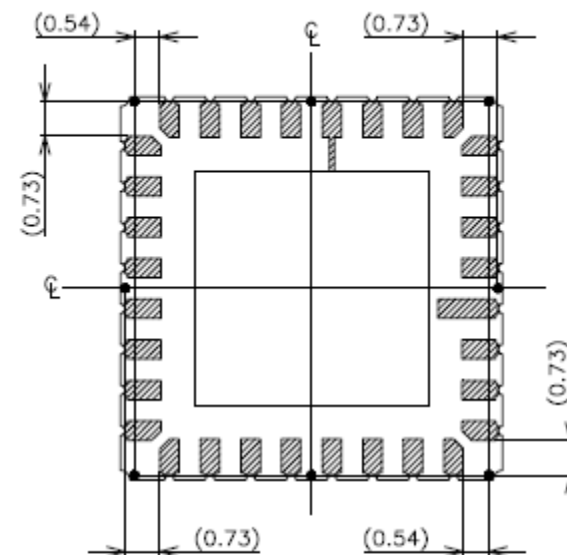
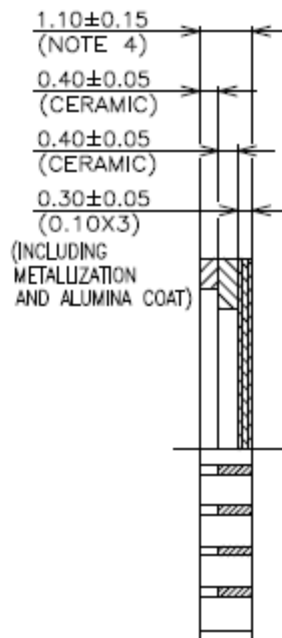
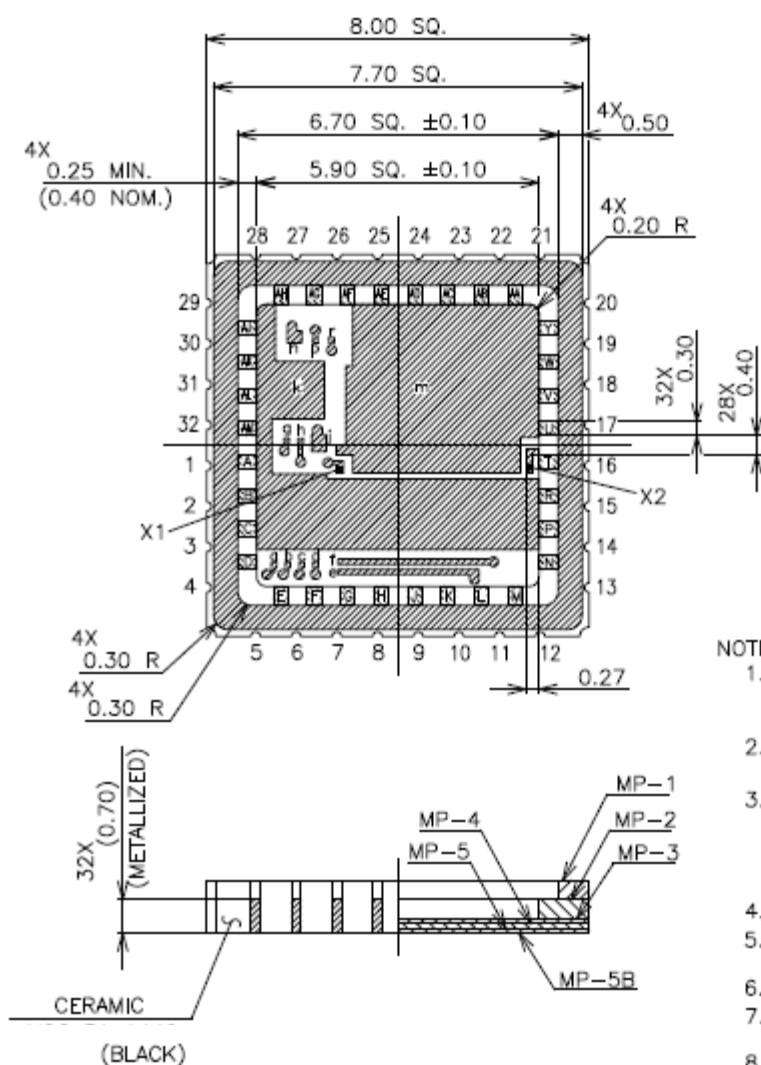


FIGURE 3
(NOTE 2)

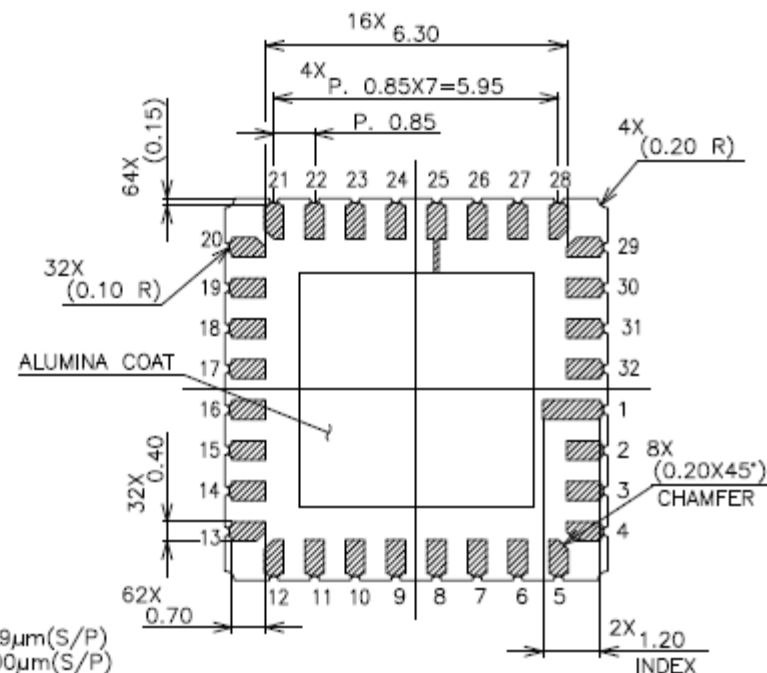
[MEASUREMENT ITEM]
CERAMIC CAMBER

[STANDARD]
80μm MAX.



NOTES:

1. PLATING THICKNESS
ELECTROLYTIC Ni : 1.27~8.89 μ m(S/P)
ELECTROLYTIC Au : 0.30~1.00 μ m(S/P)
2. SEAL AREA CAMBER, D/A CAMBER, CERAMIC CAMBER : SEE SHEET 2/8
(MEASUREMENT POINT : SEE FIGURE 1, FIGURE 2, FIGURE 3)
3. CONNECTION
(A-1-X1),(B-2),(C-3),(D-4),(E-5),(F-6),(G-7),(H-8),(J-9),(K-10),(L-e-11),(M-AK-f-j-12-30),(N-13),
(P-14),(R-15),(T-16),(U-17),(V-d-r-18),(W-c-p-19),(Y-20-X2),(AA-21),(AB-b-h-22),(AC-a-g-23),
(AD-m-24),(AE-25),(AF-k-26),(AG-27),(AH-n-28),(AL-31),(AM-32),(AJ-29-S/R)
4. TOTAL THICKNESS SHALL BE INCLUDING METALLIZATION AND EXCLUDING CAMBER.
5. EXCESS Au PLATING ON ALUMINA COAT SHALL BE ACCEPTED.
INSULATION OF ALUMINA COAT SHALL NOT BE REQUIRED.
6. PATTERN EXPOSURE BY ALUMINA COAT MISALIGNMENT SHALL BE ALLOWED.
7. ELECTRICAL OPEN AND SHORT CHECK SHALL NOT BE REQUIRED
AS LOT TOLERANCE PERCENT DEFECTIVE 0.1% PER TAPE LOT IN SINGLE UNIT.
8. HERMETICITY CHECK SHALL BE DONE BY SAMPLING
AS LOT TOLERANCE PERCENT DEFECTIVE 0.1% PER TAPE LOT IN SINGLE UNIT.
9. DIMENSIONS IN PARENTHESIS () INDICATE DESIGN VALUE AND ANY TOLERANCE NOT APPLICABLE.



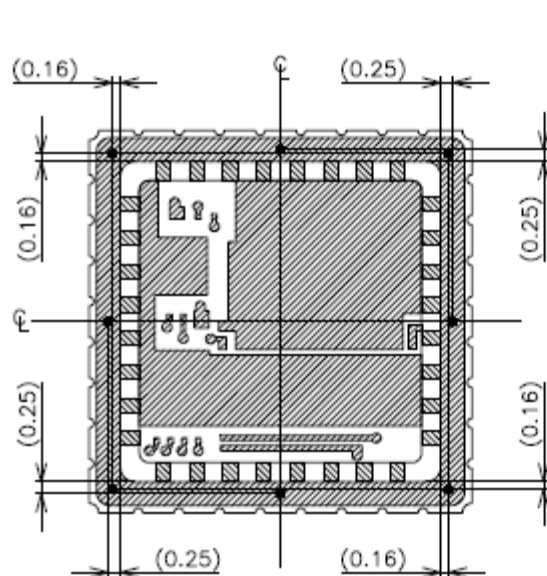


FIGURE 1
(NOTE 2)

[MEASUREMENT ITEM]	[STANDARD]
SEAL AREA CAMBER	80μm MAX.

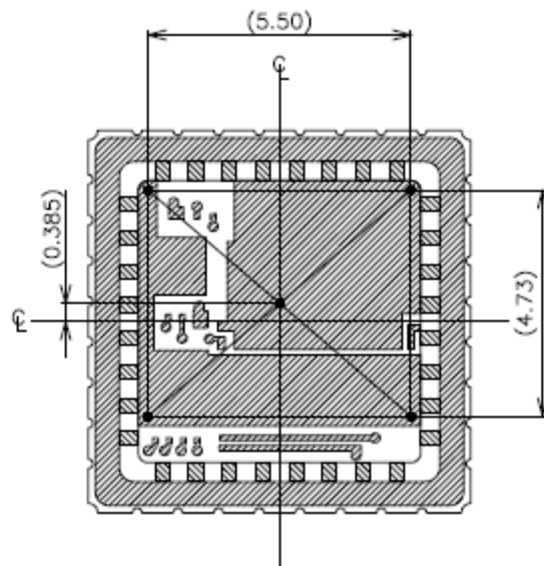


FIGURE 2
(NOTE 2)

[MEASUREMENT ITEM]	[STANDARD]
D/A CAMBER	80μm MAX.

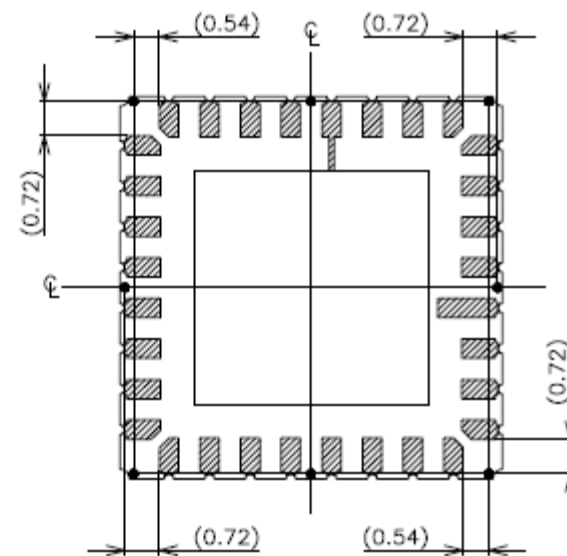


FIGURE 3
(NOTE 2)

[MEASUREMENT ITEM]	[STANDARD]
CERAMIC CAMBER	80μm MAX.